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Section 4: Threads and Context Switching

CS162

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1 \mathbf{Warmup}

Hello World 1.1

```
PTD 1234
Assume no failure
What does C print in the following code?
void* identify(void* arg) {
   pid_t pid = getpid();
   printf("My pid is %d\n", pid);
   return NULL;
}
                                                                  child killed,
pornt terminates
int main() {
   pthread_t thread;
   pthread_create(&thread, NULL, &identify, NULL);
   identify(NULL);
   return 0;
}
      My PID is 123
2
```

Vocabulary

- thread a thread of execution is the smallest unit of sequential instructions that can be scheduled for execution by the operating system. Multiple threads can share the same address space, but each thread independently operates using its own program counter.
- pthreads A POSIX-compliant (standard specified by IEEE) implementation of threads. A pthread_t is usually just an alias for "unsigned long int".
- pthread_create Creates and immediately starts a child thread running in the same address space of the thread that spawned it. The child executes starting from the function specified. Internally, this is implemented by calling the clone syscall.

```
/* On success, pthread_create() returns 0; on error, it returns an error
* number, and the contents of *thread are undefined. */
int pthread_create(pthread_t *thread, const pthread_attr_t *attr,
                   void *(*start_routine) (void *), void *arg);
```

• pthread_join - Waits for a specific thread to terminate, similar to waitpid(3).

/* On success, pthread_join() returns 0; on error, it returns an error number. */ int pthread_join(pthread_t thread, void **retval);

• pthread_yield - Equivalent to thread_yield() in Pintos. Causes the calling thread to vacate the CPU and go back into the ready queue without blocking. The calling thread is able to be scheduled again immediately. This is not the same as an interrupt and will succeed in Pintos even if interrupts are disabled.

/* On success, pthread_yield() returns 0; on error, it returns an error number. */ int pthread_yield(void);

3 Problems

assume calls succeed

3.1 Join

```
What does C print in the following code?

(Hint: There may be zero, one, or multiple answers.)

void *helper(void *arg) {
    printf("HELPER\n");
    return NULL;
}

int main() {
    pthread_t thread;
    pthread_create(&thread, NULL, &helper, NULL);
    pthread_yield();
    printf("MAIN\n");
    return 0;
}
```

Man Helper Helper Main Main

How can we modify the code above to always print out "HELPER" followed by "MAIN"?

3.2**Stack Allocation**

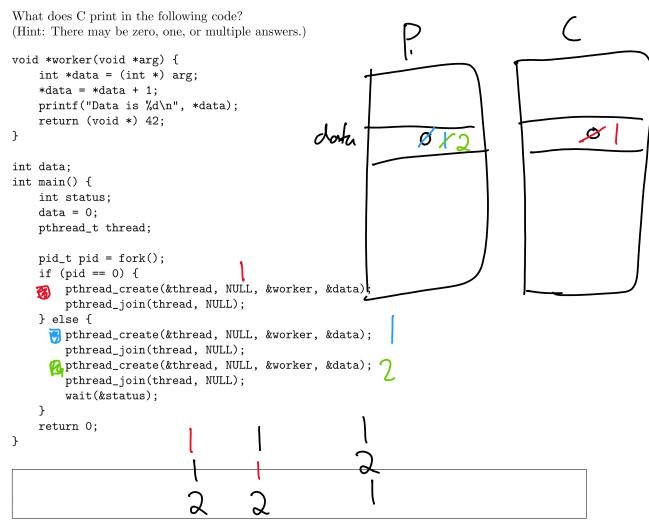
```
Stuck 1
What does C print in the following code?
void *helper(void *arg) {
    int *num = (int*) arg;
    *num = 2;
   return NULL;
                                                                  stick 2
}
                                                             ara
int main() {
    int i = 0;
   pthread_t thread;
   pthread_create(&thread, NULL, &helper, &i);
    pthread_join(thread, NULL);
    printf("i is %d\n", i);
   return 0;
}
```

3.3 Heap Allocation

What does C print in the following code?

```
void *helper(void *arg) {
    char *message = (char *) arg;
    strcpy(message, "I am the child");
    return NULL;
}
int main() {
    char *message = malloc(100);
    strcpy(message, "I am the parent");
    pthread_t thread;
    pthread_create(&thread, NULL, &helper, message);
    pthread_join(thread, NULL);
    printf("%s\n", message);
    return 0;
}
```

3.4 Threads and Processes



How would you retrieve the return value of worker? (e.g. "42")

3.5 Context Switching

| Refer to the "Pintos Context Switch" section at the end of this discussion worksheet to answer thes questions: | | | |
|---|--|--|--|
| How many stacks are involved in a context switch? Identify the purpose of each stack. | | | |
| | | | |
| | | | |
| The value of SWITCH_CUR is 20. The value of SWITCH_NEXT is 24. With this information, please drawthe stack frame of switch_threads for a thread that is about to switch the stack pointer to the next thread's stack. Your stack frame should include the arguments cur and next. | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| In addition to the code inside switch_threads, what other actions are required to perform a contex switch between 2 user program threads? | | | |
| | | | |
| In order to perform a context switch, the kernel must copy all of a thread's registers onto the CPU registers. How is the <code>%eip</code> (instruction pointer) register copied onto the CPU? Identify which instructio is responsible for this. | | | |
| | | | |
| | | | |
| | | | |

3.6 Interrupt Handlers

| Refer to the "Pintos Interrupt Handler" section at the end of this discussion worksheet to answer these questions: | | | |
|--|-----------------------|--|--|
| What do the instructions pushal and popal do? | | | |
| | | | |
| | | | |
| The interrupt service routine (ISR) must run with the kernel's stack. Why is which instruction is responsible for switching the stack pointer to the kernel stack? | | | |
| | | | |
| The pushal instruction pushes 8 values onto the stack (32 bytes). With this draw the stack at the moment when "call intr_handler" is about to be executed | | | |
| | | | |
| | | | |
| | | | |
| | | | |
| What is the purpose of the "pushl %esp" instruction that is right before "call | intr_handler"? | | |
| | | | |
| Inside the intr_exit function, what would happen if we reversed the order of th | e 5 pop instructions? | | |
| | | | |
| | | | |
| | | | |
| | | | |

3.7 Pintos Context Switch

```
3 #### struct thread *switch_threads (struct thread *cur, struct thread *next);
 4 ####
 5 #### Switches from CUR, which must be the running thread, to NEXT,
 6 #### which must also be running switch_threads(), returning CUR in
 7 #### NEXT's context.
 8 ####
 9 #### This function works by assuming that the thread we're switching
10 #### into is also running switch_threads(). Thus, all it has to do is
11 #### preserve a few registers on the stack, then switch stacks and
12 #### restore the registers. As part of switching stacks we record the
13 #### current stack pointer in CUR's thread structure.
15 .globl switch_threads
16 .func switch_threads
17 switch_threads:
18
           # Save caller's register state.
19
20
           # Note that the SVR4 ABI allows us to destroy %eax, %ecx, %edx,
           # but requires us to preserve %ebx, %ebp, %esi, %edi.
21
22
           # [SysV-ABI-386] pages 3-11 and 3-12 for details.
23
24
           # This stack frame must match the one set up by thread_create()
25
           # in size.
26
           pushl %ebx
27
           pushl %ebp
28
           pushl %esi
29
           pushl %edi
30
           # Get offsetof (struct thread, stack).
31
32 .globl thread_stack_ofs
           mov thread_stack_ofs, %edx
33
34
35
           # Save current stack pointer to old thread's stack, if any.
           movl SWITCH_CUR(%esp), %eax
36
37
           movl %esp, (%eax, %edx, 1)
38
39
           # Restore stack pointer from new thread's stack.
40
           movl SWITCH_NEXT(%esp), %ecx
           movl (%ecx,%edx,1), %esp
41
42
43
           # Restore caller's register state.
           popl %edi
44
           popl %esi
45
46
           popl %ebp
47
           popl %ebx
48
           ret
49 .endfunc
```

3.8 Pintos Interrupt Handler

```
1 /**
 2 * An example of an entry point that would reside in the interrupt
 3 * vector. This entry point is for interrupt number 0x30.
 4 */
 5 .func intr30_stub
 6 intr30_stub:
 7
         pushl %ebp
                        /* Frame pointer */
 8
         pushl $0
                        /* Error code */
 9
         pushl $0x30
                        /* Interrupt vector number */
10
         jmp intr_entry
11 .endfunc
12 /* Main interrupt entry point.
13
14
      An internal or external interrupt starts in one of the
15
      intrNN_stub routines, which push the 'struct intr_frame'
16
      frame_pointer, error_code, and vec_no members on the stack,
17
      then jump here.
18
      We save the rest of the 'struct intr_frame' members to the
19
20
      stack, set up some registers as needed by the kernel, and then
      call intr_handler(), which actually handles the interrupt.
21
22
23
      We "fall through" to intr_exit to return from the interrupt.
24 */
25 .func intr_entry
26 intr_entry:
27
           /* Save caller's registers. */
28
           pushl %ds
29
           pushl %es
30
           pushl %fs
31
           pushl %gs
           pushal
32
33
34
           /* Set up kernel environment. */
35
                                   /* String instructions go upward. */
36
          mov $SEL_KDSEG, %eax
                                   /* Initialize segment registers. */
37
          mov %eax, %ds
           mov %eax, %es
38
           leal 56(%esp), %ebp
39
                                   /* Set up frame pointer. */
40
41
           /* Call interrupt handler. */
42
          pushl %esp
43 .globl intr_handler
44
           call intr_handler
45
           addl $4, %esp
46 .endfunc
```

```
48 /* Interrupt exit.
49
50
      Restores the caller's registers, discards extra data on the
51
      stack, and returns to the caller.
52
53
      This is a separate function because it is called directly when
54
      we launch a new user process (see start_process() in
      userprog/process.c). */
55
56 .globl intr_exit
57 .func intr_exit
58 intr_exit:
59
           /* Restore caller's registers. */
60
           popal
61
           popl %gs
62
           popl %fs
63
           popl %es
64
           popl %ds
65
66
           /* Discard 'struct intr_frame' vec_no, error_code,
67
              frame_pointer members. */
68
           addl $12, %esp
69
70
           /* Return to caller. */
71
           iret
72 .endfunc
```